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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
ADLER, Eric

Serial No. New

Filed: Herewith

For: **Capacitor Having Sidewall Spacer
Protecting the Dielectric Layer and
Method of Fabricating the Same**

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Attorney Docket No. **BUR919990222US2**

Examiner: **Unknown**

Group Art Unit: **Unknown**

J1000 U.S. PTO
10/057185
01/25/02

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.97(b) and 1.98 (d)**

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

The above-entitled application is a divisional application of U.S. Application Serial No. 09/616,951 filed on July 14, 2000. Pursuant to 37 C.F.R. § 1.97 (b), Applicants respectfully request that the references of record in the parent case be considered in this divisional application. These references are listed in the attached Substitute Form PTO 1449. Pursuant to 37 C.F.R. § 1.98(d), copies of these references are not provided with this paper.

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I hereby certify that the application/correspondence attached hereto is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to:


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No fee is believed to be due; however, should any fee be required, please charge that fee to
IBM Deposit Account Number 09-0456.

Respectfully submitted,



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